

Date: December 3, 1993

Honorable Commissioner of Patents and Trademarks
Washington, D.C. 20231

Transmitted herewith for filing is the patent application of:

For: METHOD OF FABRICATING SEMICONDUCTOR DEVICES
AND APPARATUS FOR PROCESSING A SEMICONDUCTOR

[X] 19 pages of specification and claims

[X] 5 sheets of informal drawings (Figures 1-6)

[X] An assignment of the invention to Semiconductor Energy Laboratory Co., Ltd.

[X] Declaration and Power of Attorney.

☒ [X] Priority is claimed based on foreign application(s)

No(s):4-350546 and Not Yet Assigned

Filed on: December 4, 1992 and November 5, 1993

Filed in: Japan

Certified copy(s) ☐ attached; ☐ filed on

_____ in prior U.S.S.N. _____
filed _____

[] A verified statement to establish small entity status under 37 CFR 1.9 and 37 CFR 1.27.

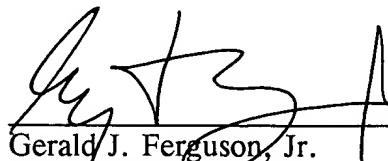
[X] The filing fee has been calculated as shown below.

<u>For:</u>	<u>No. Filed</u>	<u>No. Extra</u>	<u>Small/Large Entity</u> <u>Rate</u>	<u>Fee</u>
Basic Fee				\$710.00
Total Claims	<u>20</u> - 20 =	0	x 11/22	
Indep Claims	<u>3</u> - 3 =	0	x 37/74	
[] Multiple Dependent Claim			+ 115/230	
			TOTAL	\$710.00
[X] Assignment Recordation Fee				\$ <u>40.00</u>
TOTAL FEE				\$750.00

[X] A check in the amount of \$750.00 to cover the filing fee is enclosed.

[X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20(d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,



Gerald J. Ferguson, Jr.
Registration No. 23,016

Sixbey, Friedman, Leedom & Ferguson, P.C.
2010 Corporate Ridge, Suite 600
McLean, Virginia 22102
(703) 790-9110